

Amendments to the Claims:

This listing of claims replaces all prior versions and listings of claims in the application:

Listing of Claims:

1-11. (Cancelled)

12. (Currently Amended) A protective circuit comprising:

a first transceiver to connect to a first network section;

a second transceiver to connect to a second network section; and

a logic isolation circuit to isolate a network section from a bus system network using logic when a fault state is recognized in the network section, the logic isolation circuit comprising

a reception ~~multiplexers~~ multiplexer having a first signal input connected to the first transceiver receiver and a second signal input to receive a logically recessive transmission signal; and

a transmission ~~multiplexers~~ multiplexer having a signal output connected to one of the transceiver transmitters and a first signal input connected to a signal output of the reception ~~multiplexers~~ multiplexer for the transceiver receiver, wherein a logically recessive transmission signal is applied to the second signal input of the ~~multiplexers~~ multiplexer;

~~a first transceiver to connect to a first network section;~~

~~a second transceiver to connect to a second network section; and~~

wherein a first transceiver receiver in the first transceiver recognizes a fault state in the first network section, and a second transceiver receiver in the second transceiver recognizes a fault state in the second network section, and the logic isolation circuit includes logic signal inputs to connect to both receivers and logic signal outputs to connect to transceiver transmitters.

13. (Canceled)

14. (Previously Presented) The protective circuit of claim 12 wherein the logic isolation circuit blocks a dominant transmission signal from the first network section.

15. (Previously Presented) The protective circuit of claim 12 wherein the logic isolation circuit blocks a dominant transmission signal to the second network section.

16. (Previously Presented) The protective circuit of claim 12 further comprising a fault bus to connect the logic isolation circuit to control nodes in the bus system network, wherein the fault bus provides information data to the control nodes indicating that the network section recognized as faulty has been isolated from the bus system network.

17. (Currently Amended) The protective circuit of claim [[13]] 12 wherein the first signal inputs of the transmission multiplexers [[is]] are connected to the signal outputs of the reception multiplexers with DC decoupling.

18-25. Cancelled

26. (New) A protective circuit for an access arbitrated bus system network, the protective circuit comprising:

a fault recognition device to recognize a fault state in a network section in the bus system network by monitoring voltage levels on bus lines in the bus system network; and

a switching device to isolate the network section from the bus system network by switching bus lines when a fault state has been recognized in the network section, the switching device including a plurality of switches connected in parallel, wherein a respective switch is provided for each bus line, wherein the switches comprise semiconductor switches configured to block signals in both signal directions.